

REMARKS

Claims 1-20 remain in the application for consideration of the Examiner.

Reconsideration and withdrawal of the outstanding rejections and objections are respectfully requested in light of the above amendments and following remarks.

Claims 1-10 and 16 were objected to because of informalities.

By the instant amendment, it is respectfully submitted that these informalities have been overcome.

It is respectfully submitted that Claims 1-20 are now free from informalities.

Claims 1-5, 8-15, and 18-20 were rejected under 35 U.S.C. § 103 as being unpatentable over Bjerede in view of Dent; Claims 6 and 16 were rejected under 35 U.S.C. § 103 as being unpatentable over Bjerede in view of Dent and Khlai; and Claims 7 and 17 were rejected under 35 U.S.C. § 103 as being unpatentable over Bjerede in view of Dent in view of Boesch.

These rejections are respectfully traversed.

It is respectfully submitted that Bjerede does not disclose or suggest the presently claimed invention including the first frequency synthesizer coupled to the second input of the IF processor for providing the first combining signal of one of the plurality of possible frequency separated from one another by a raster component of the desired frequency channel spacing in independent Claim 1, albeit defined as combining the first combining signal with the baseband signal to produce a IF signal in independent Claim 11.

The Examiner alleges that Bjerede alleges the first frequency synthesizer at element 100 and element 9.

The Examiner's attention is directed to column 15, lines 15-25 of Bjerede where Bjerede discloses that all the frequencies generated by the local oscillator 10 and the frequencies of the signal provided to mixer 106 and the base band processor are all integers of 9.6 MHz.

Oscillator 100 does not provide signals to IF processor 28.

Dent does not disclose or suggest the presently claimed invention including the first frequency synthesizer coupled to the second input of the IF processor for providing the first combining signal of one of the plurality of possible frequencies separated by one another by a raster component of the desired frequency channel spacing in the various forms in independent Claims 1 and 11.

Dent does not disclose a processor.

Whether or not Khlal discloses a PLL frequency synthesizer and one of ordinary skill in the art would consider modifying the teachings of either Bjerede or Dent is of no moment since the resulting construction would still in no way disclose or suggest the presently claimed invention.

Furthermore, whether or not Boesch discloses phase locked loops for WCDMA and whether or not one of ordinary skill in the art would consider modifying either Bjerede or Dent is of no moment since the resulting construction would still in no way disclose or suggest the presently claimed invention.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant response places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicant petitions for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



W. Daniel Swayze, Jr.
Attorney for Applicant
Reg. No. 34,478

Texas Instruments Incorporated
P.O. Box 655474, MS 3999
Dallas, TX 75265
(972) 917-5633